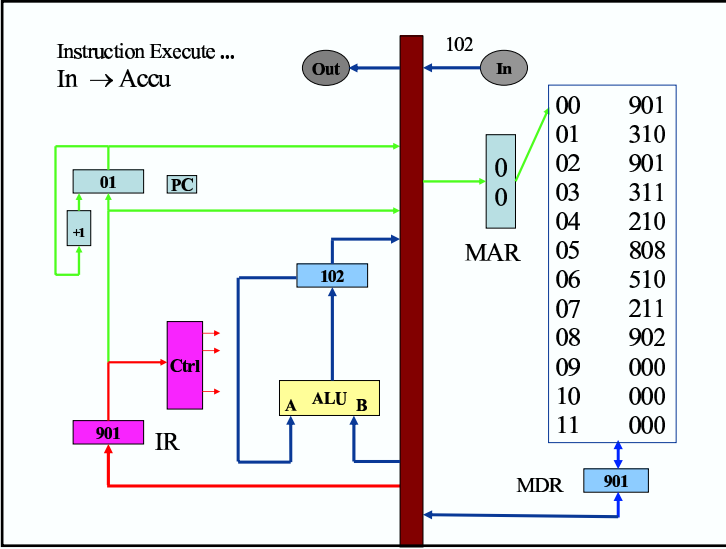
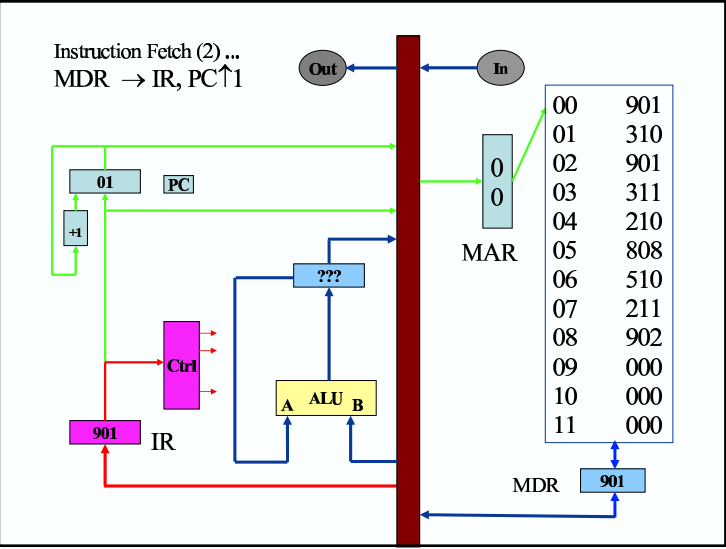
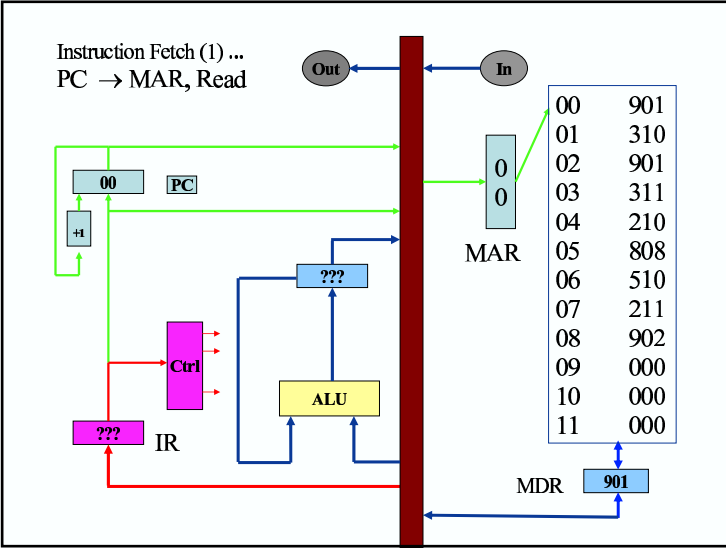
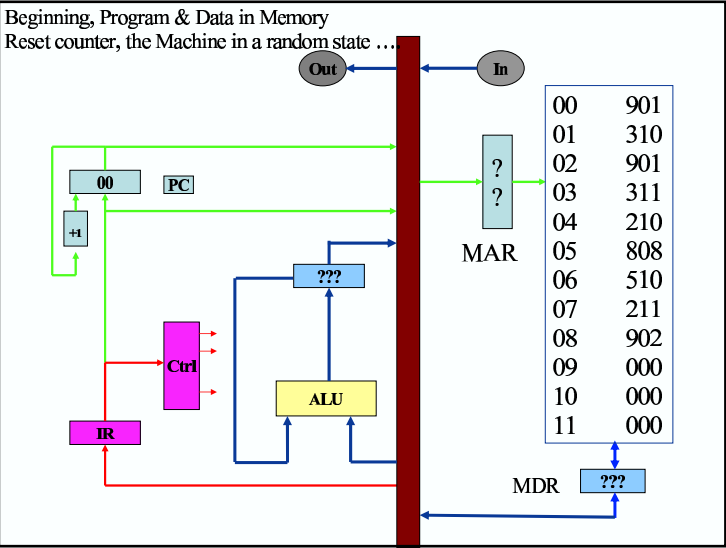
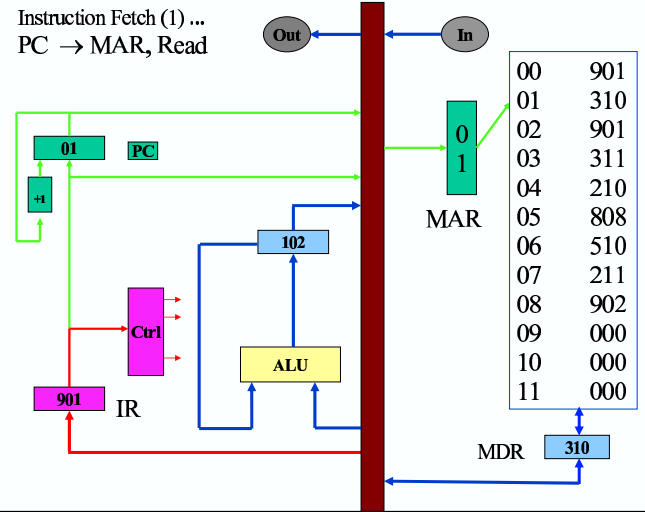




☺ Next instruction: 01

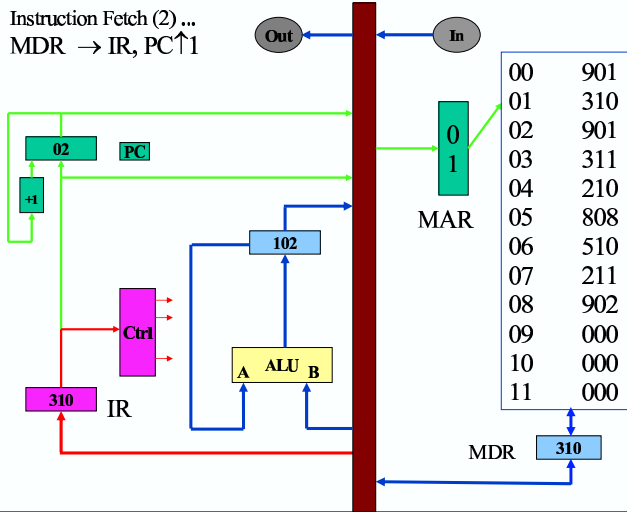
41

Instruction Fetch (1) ...
PC → MAR, Read



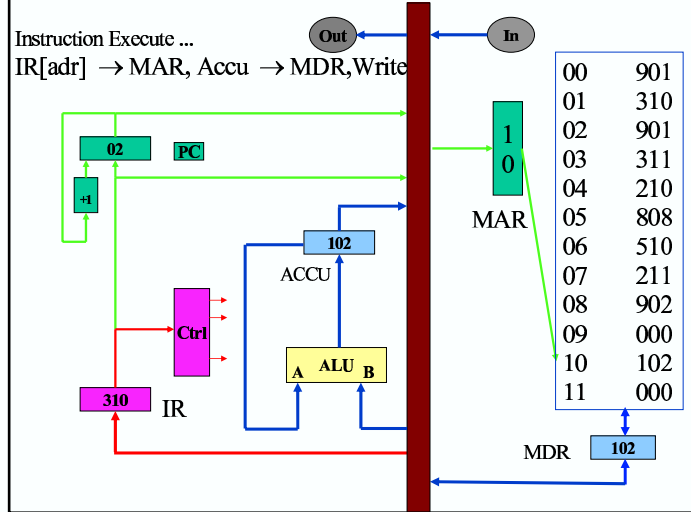
42

Instruction Fetch (2) ...
MDR → IR, PC↑1



43

Instruction Execute ...
IR[adr] → MAR, Accu → MDR, Write

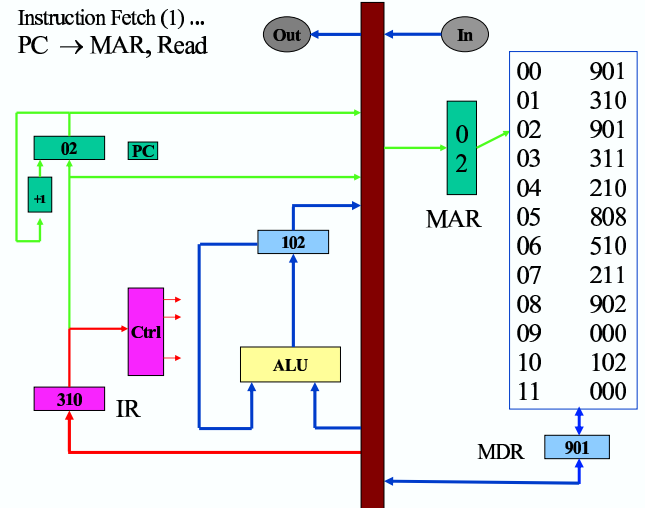


44

☺ Next instruction: 02

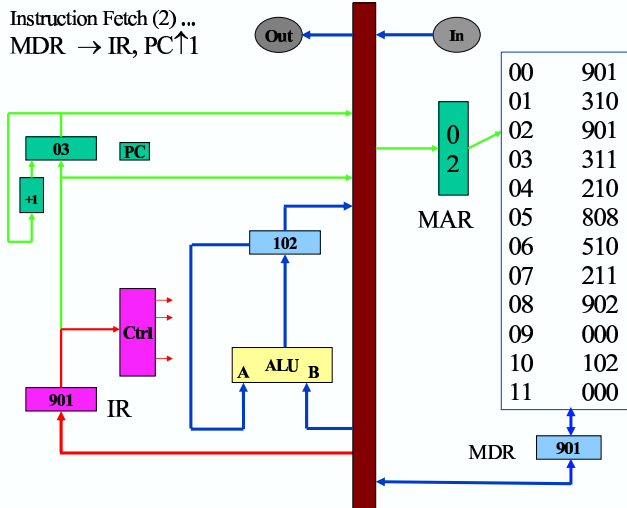
45

Instruction Fetch (1) ...
PC → MAR, Read



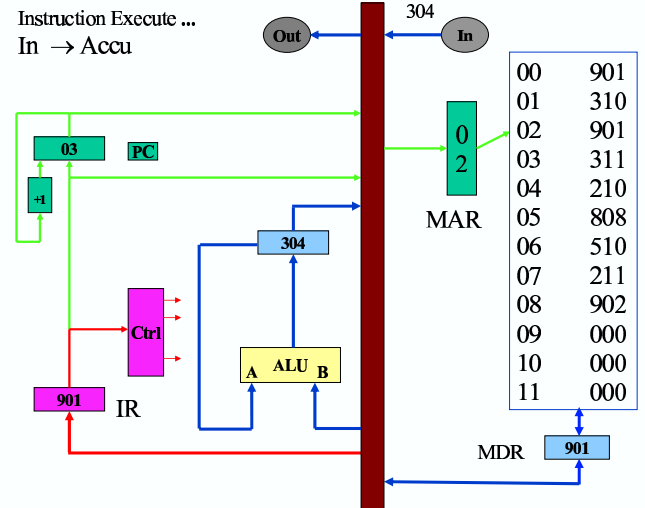
46

Instruction Fetch (2) ...
MDR → IR, PC↑1



47

Instruction Execute ...
In → Accu

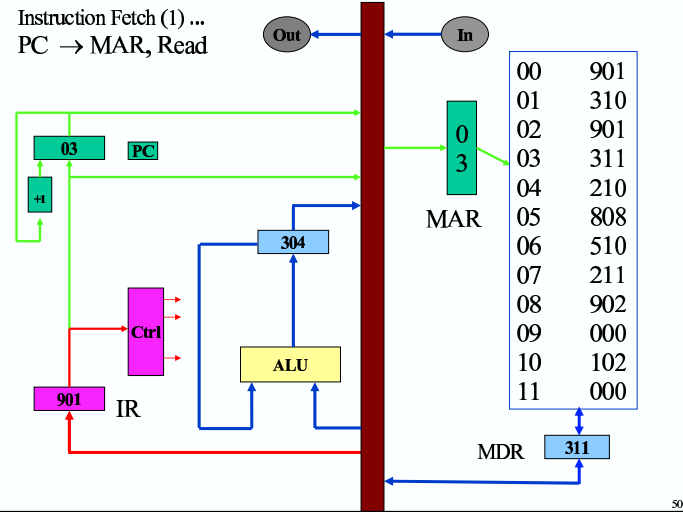


48

☺ Next instruction: 03

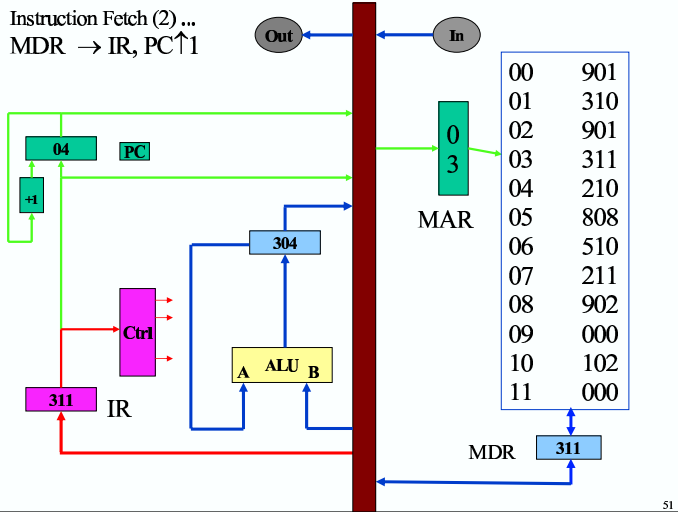
49

Instruction Fetch (1) ...
PC → MAR, Read



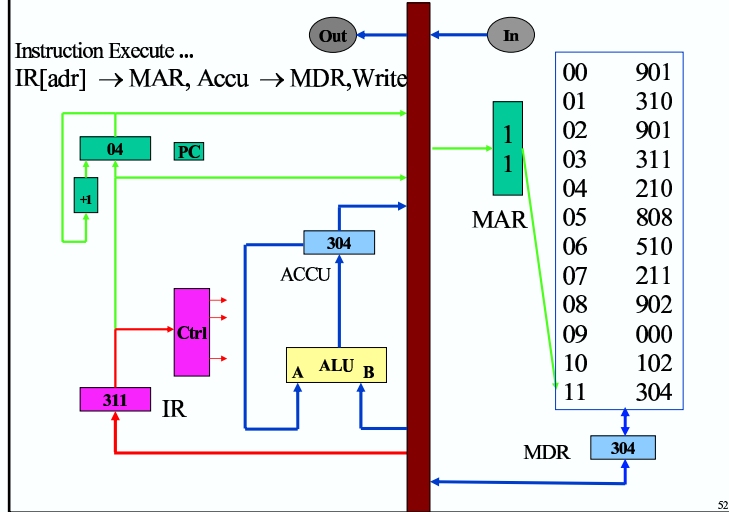
50

Instruction Fetch (2) ...
MDR → IR, PC↑1



51

Instruction Execute ...
IR[adr] → MAR, Accu → MDR, Write

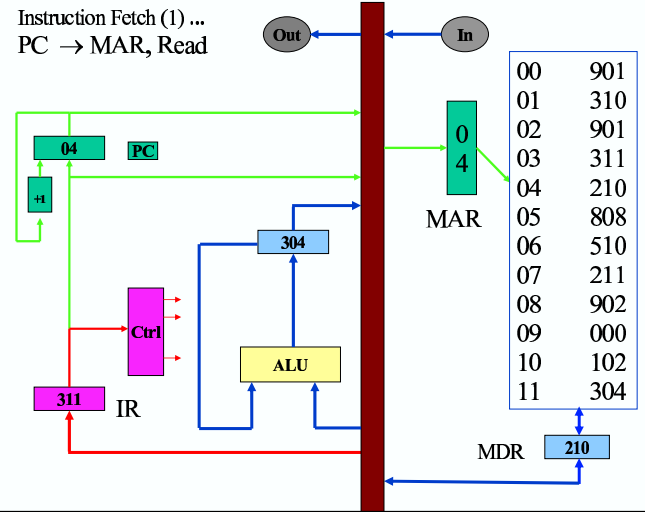


52

☺ Next instruction: 04

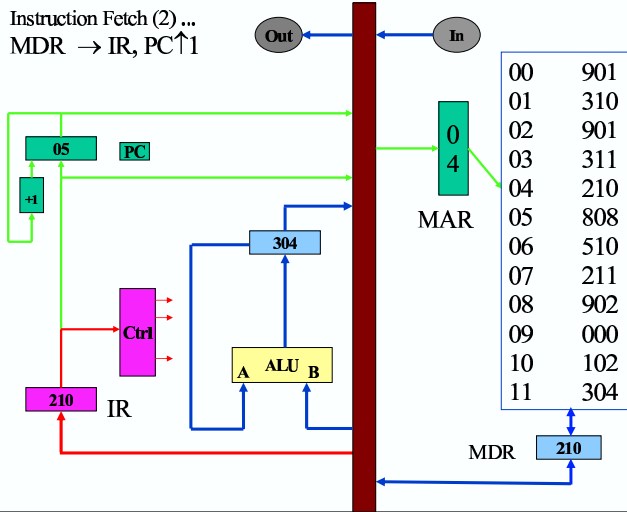
53

Instruction Fetch (1) ...
PC → MAR, Read



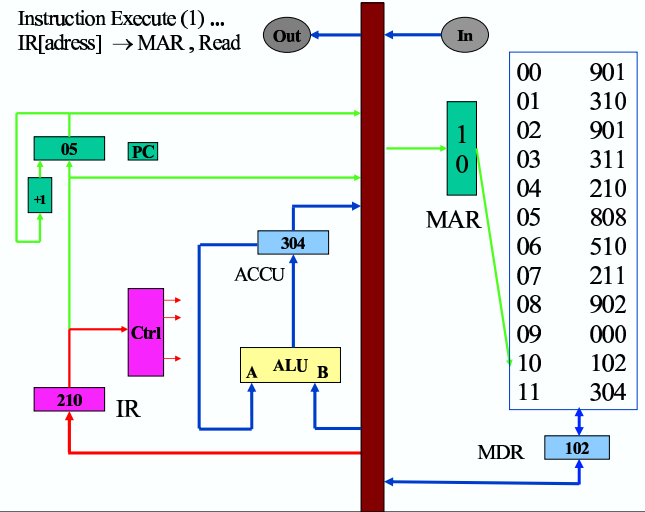
54

Instruction Fetch (2) ...
MDR → IR, PC↑1

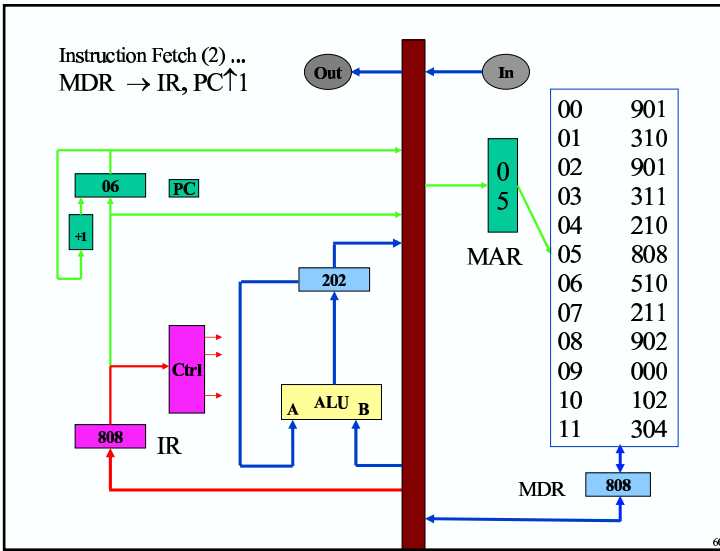
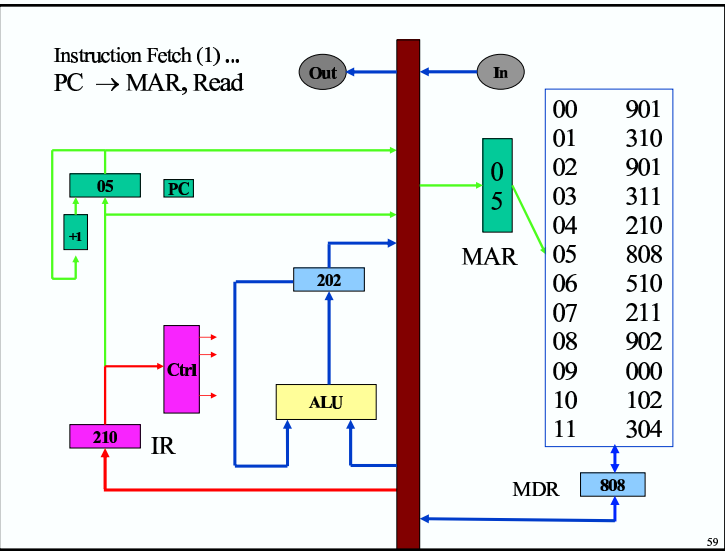
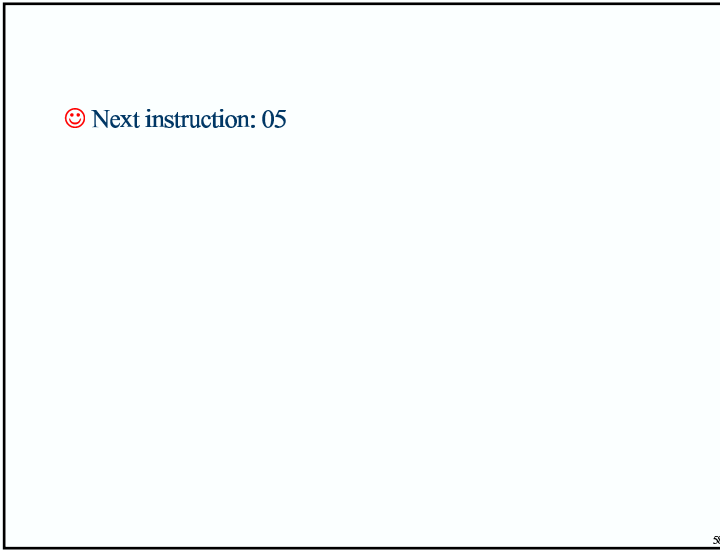
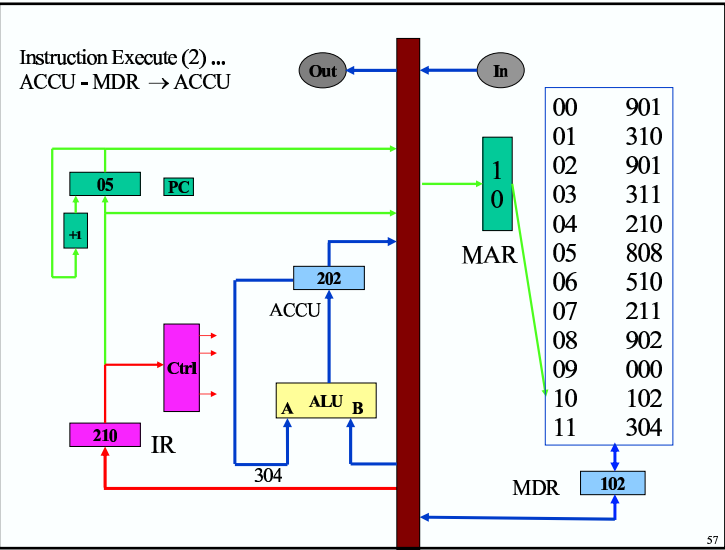


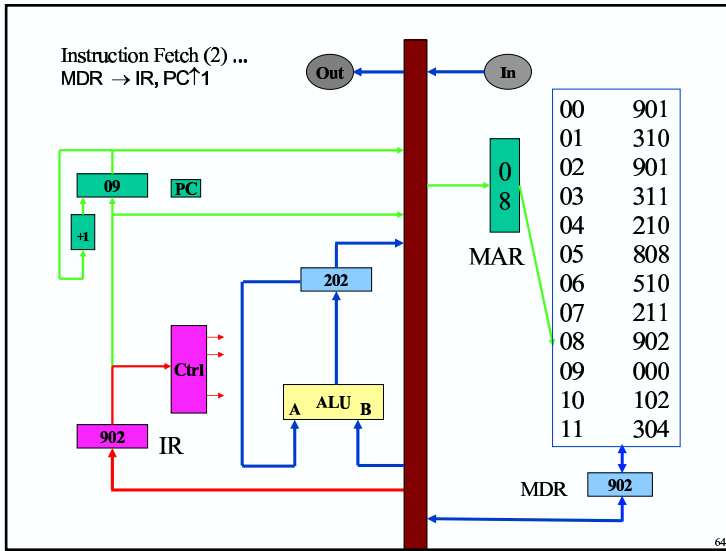
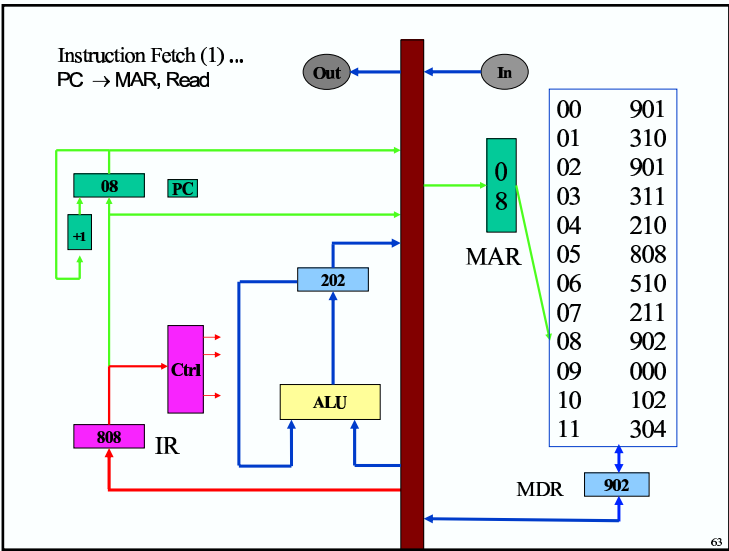
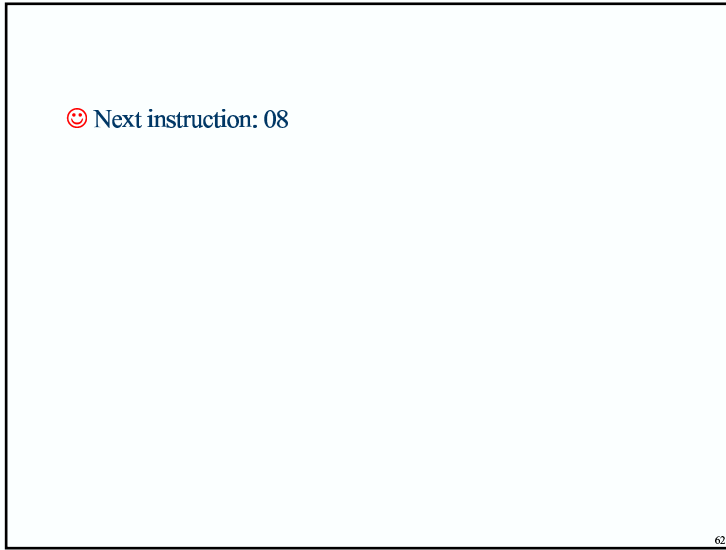
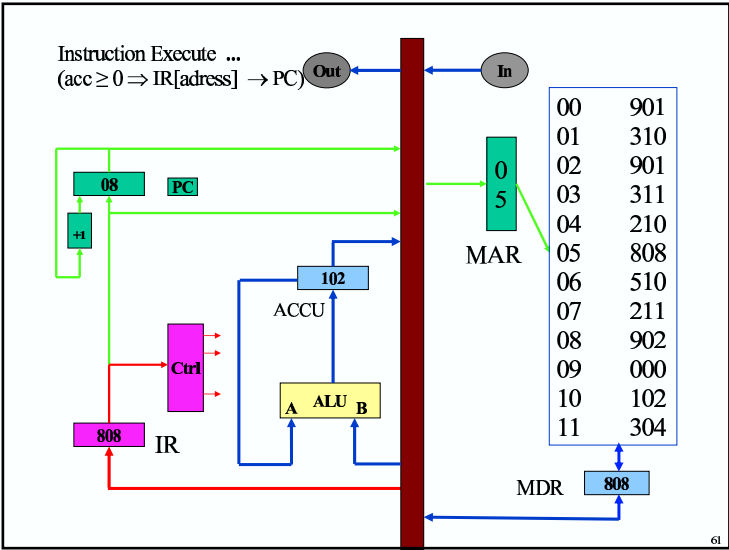
55

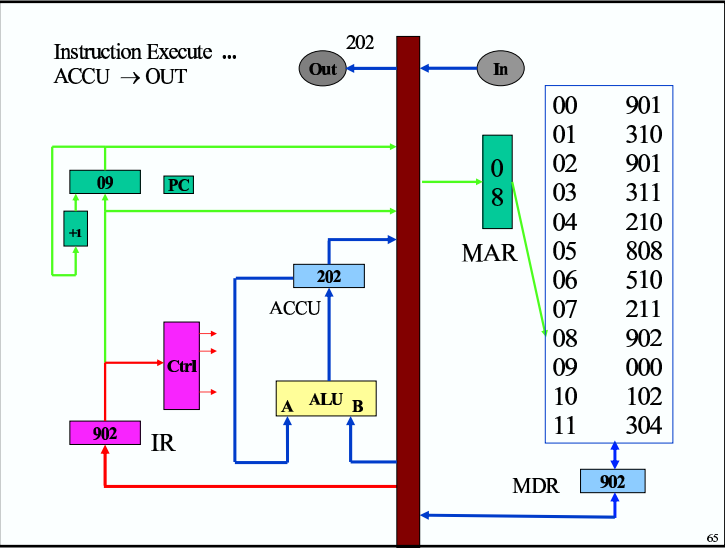
Instruction Execute (1) ...
IR[address] → MAR, Read



56







☺ Next instruction: 09

66

